



Docket No.: M4065.0741/P741  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Kit Fai Chan et al.

Application No.: 10/733,474

Filed: December 12, 2003

Art Unit: N/A

For: CURRENT MODE LOGIC SCHEME AND  
CIRCUIT FOR MATCH LINE SENSE  
AMPLIFIER DESIGN USING CONSTANT  
CURRENT BIAS CASCODE CURRENT  
MIRRORS

Examiner: Not Yet Assigned

**SUBMISSION OF FORMAL DRAWINGS**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Submitted herewith is one set (six sheets, six figures) of formal drawings for filing in the above-identified patent application. Kindly substitute the enclosed formal drawings for the informal drawings submitted with the originally filed application.

Dated: February 25, 2004

Respectfully submitted,

By

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